

Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

Claims 1 and 2 (Canceled).

3. (Previously presented) The disk enclosure of claim 5, wherein the first plurality of elements includes at least one of a first temperature sensor, a first memory, and a first backplane controller.
4. (Previously presented) The disk enclosure of claim 3, wherein the backplane controller is coupled to a first port bypass circuit, the first port bypass circuit operable to bypass a first disk drive.
5. (Previously presented) A disk enclosure comprising:
  - a first enclosure controller coupled to first and second buses;
  - a first plurality of elements coupled to the first bus;
  - a first voltage circuit powering the first enclosure controller and the first plurality of elements in a first power domain;
  - a second enclosure controller coupled to third and fourth buses;
  - a second plurality of elements coupled to the fourth bus;
  - a second voltage circuit powering the second enclosure controller and the second plurality of elements in a second power domain;
  - a first switch coupled between the first and the third buses, the first switch operable to de-couple the first and the third buses when the voltage output from the second voltage circuit falls below a predetermined threshold; and
  - a second switch coupled between the second and the fourth buses, the second switch operable to de-couple the second and the fourth buses when the voltage output from the first voltage circuit falls below the predetermined threshold.

Claim 6 (Canceled).

7. (Previously presented) The disk enclosure of claim [4], wherein the second plurality of elements includes at least one of a second temperature sensor, a second memory, and a second backplane controller.

8. (Previously presented) The disk enclosure of claim 7, wherein the second backplane controller is coupled to a second port bypass circuit, the second port bypass circuit operable to bypass a second disk drive.

9. (Withdrawn) The disk enclosure of claim 5, wherein:

the first enclosure controller is coupled to a fifth bus;

the second enclosure controller is further coupled to a sixth bus;

a third switch coupled between the fifth bus and a seventh bus, the third switch operable to de-couple the fifth and the seventh buses when the voltage output from the first voltage circuit falls below the predetermined threshold; and

a fourth switch coupled between the sixth bus and the seventh bus, the fourth switch operable to de-couple the sixth and seventh buses when the voltage output from the second voltage circuit falls below the predetermined threshold.

10. (Withdrawn) The disk enclosure of claim 9, wherein the seventh bus is further coupled to a third plurality of elements.

11. (Withdrawn) The disk enclosure of claim 10, wherein the third plurality of elements includes at least one of a third temperature sensor, a third memory, a third backplane controller, and an I/O expander.

12. (Withdrawn) The disk enclosure of claim 11, wherein the I/O expander is coupled to at least one battery.

13. (Withdrawn) The disk enclosure of claim 11, wherein the I/O expander is coupled to at least one power supply.

Claims 14 to 26 (Canceled).

27. (Currently amended) The disk enclosure of claim [[1]] 5, wherein the first and the second buses comprise I2C buses.

28. (Canceled).